

Fig.1 (Prior Art)
Alpha Format Packet

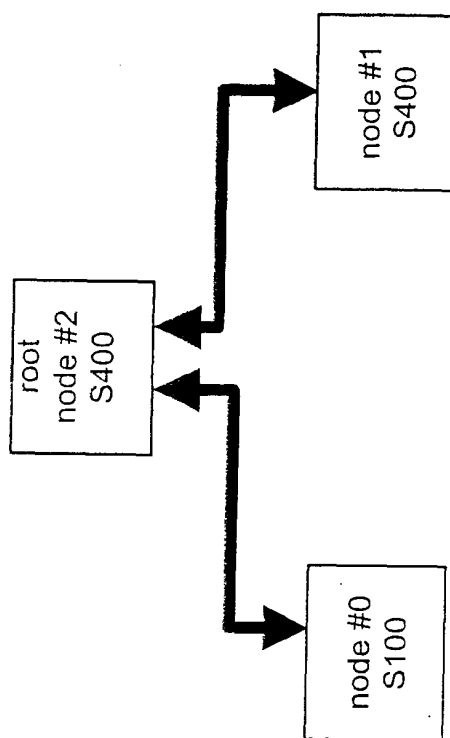


Fig. 2 (Prior Art)
Legacy Compatibility Example

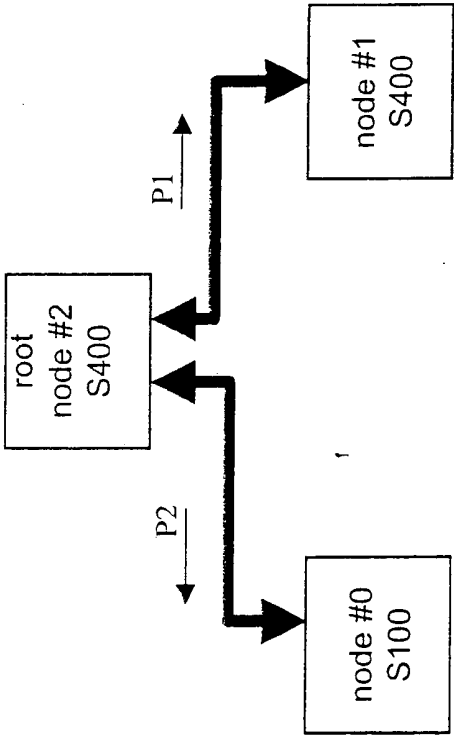
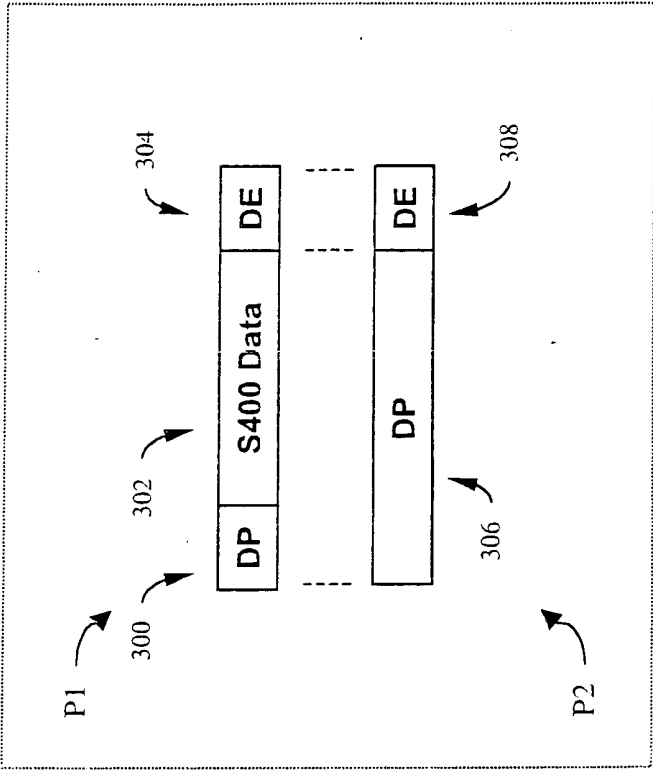



Fig. 3 (Prior Art)
Legacy Speed Filtering Example

400



$\left\{ \underline{Sb1} \ \underline{DP1} \ \underline{B1} \ \dots \ \underline{Bn} \ \underline{DE1} \ \underline{DE2} \right\}$

Fig. 4 (Prior Art)
P1394b Packet Format

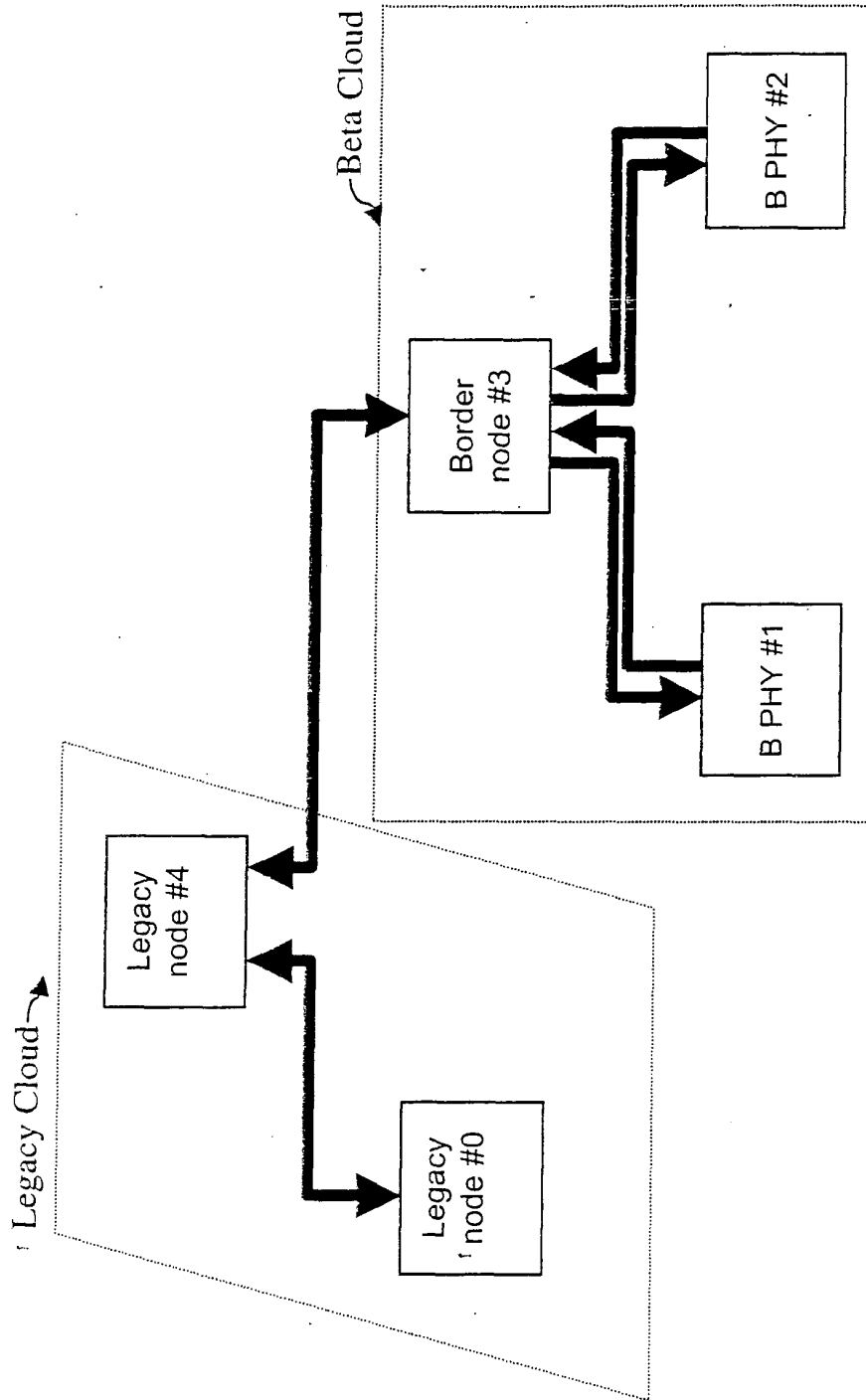


Fig. 5 (Prior Art)
Data Communications System Having both
P1394b and Legacy Devices

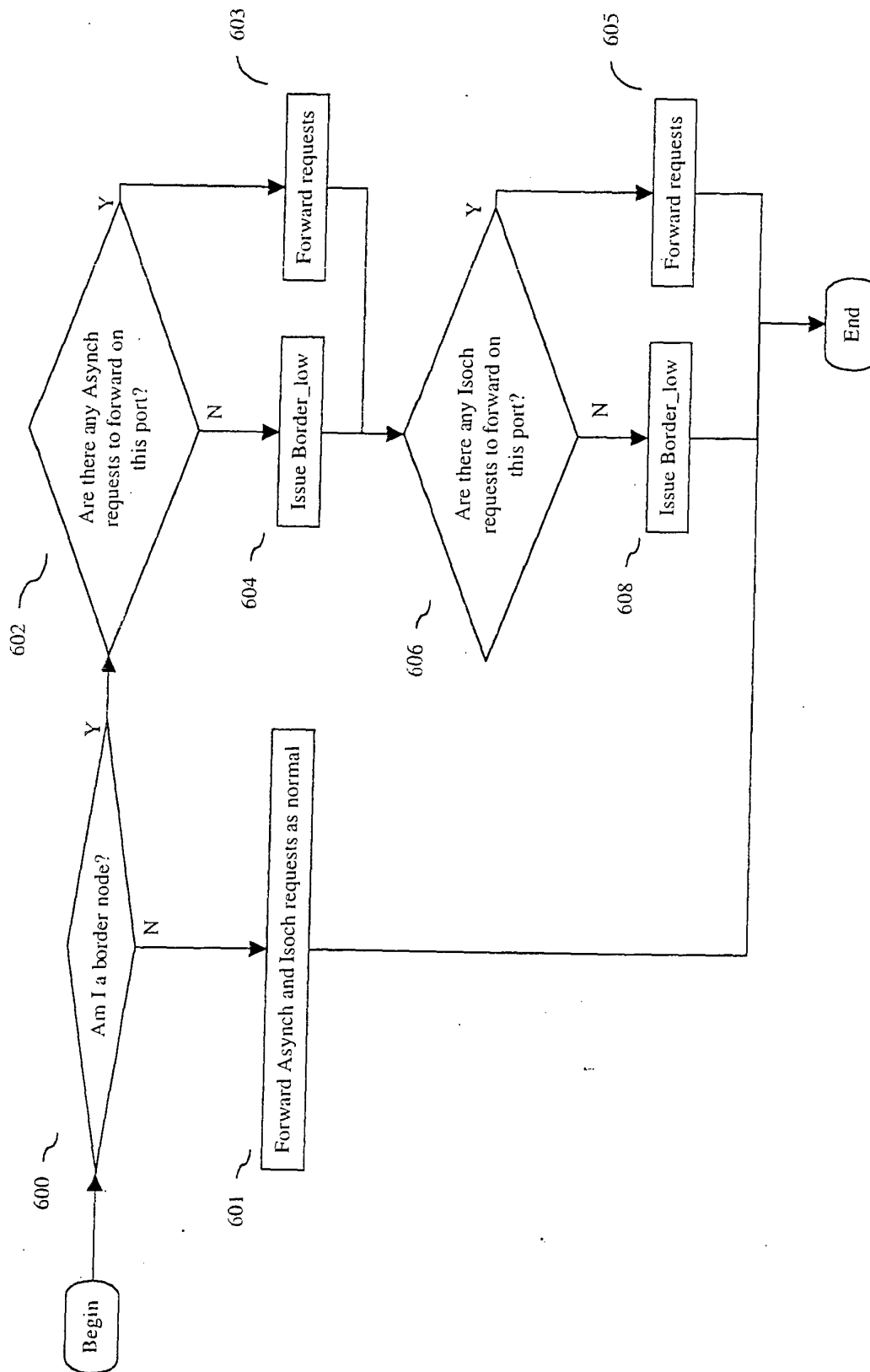


Fig. 6
 Detection and Communication of Hybrid Bus
 According to the Present Invention

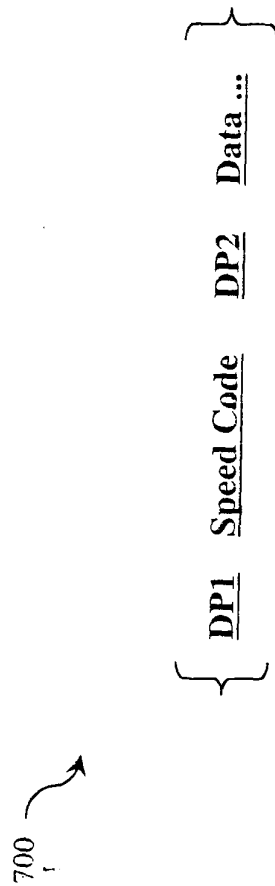


Fig. 7 (Prior Art)
Legacy Format Packet

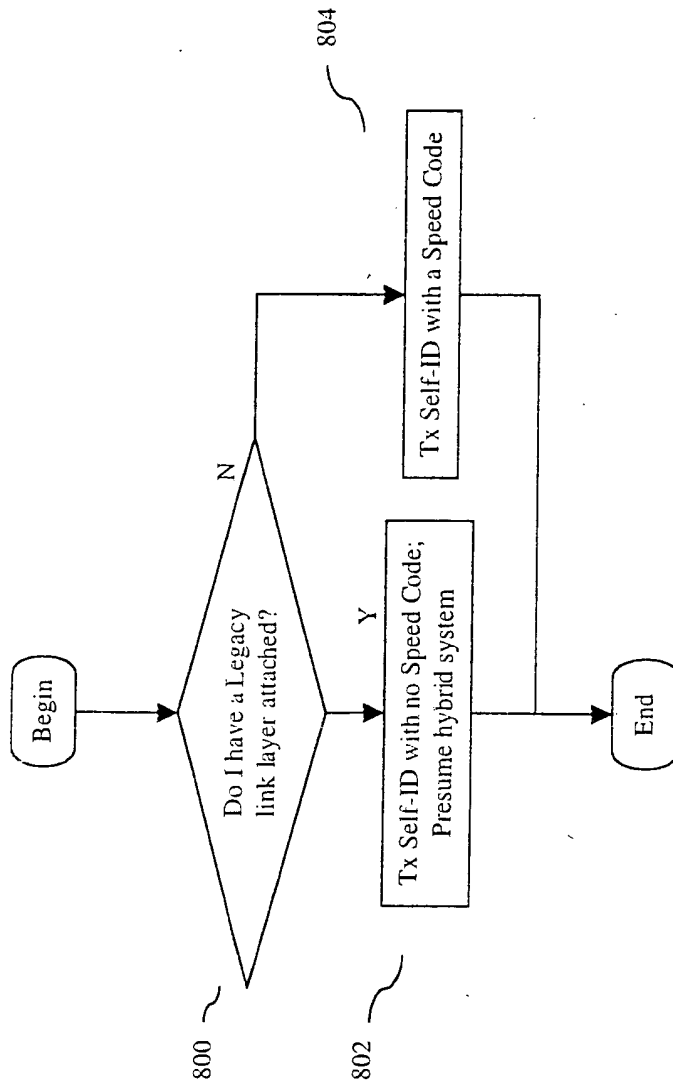


Fig. 8
Detection and Communication of a Legacy Link Layer
According to the Present Invention

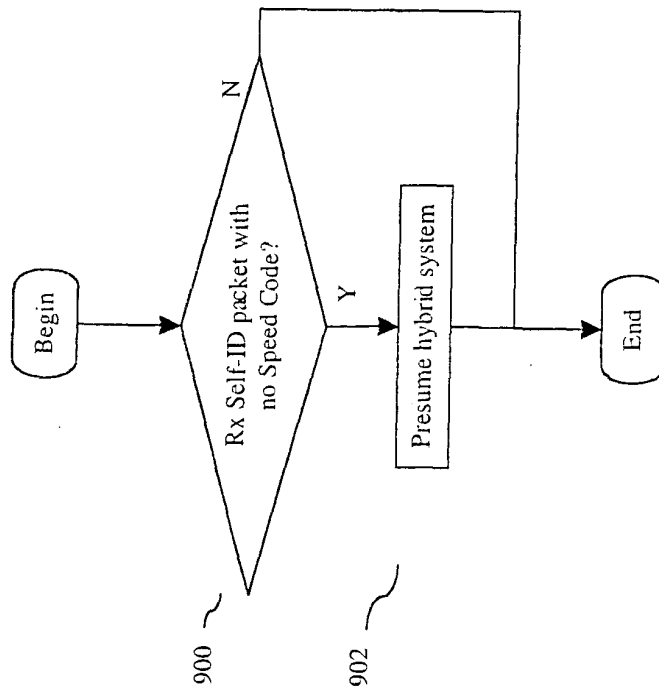


Fig. 9
Detection and Communication of Hybrid Bus
According to the Present Invention

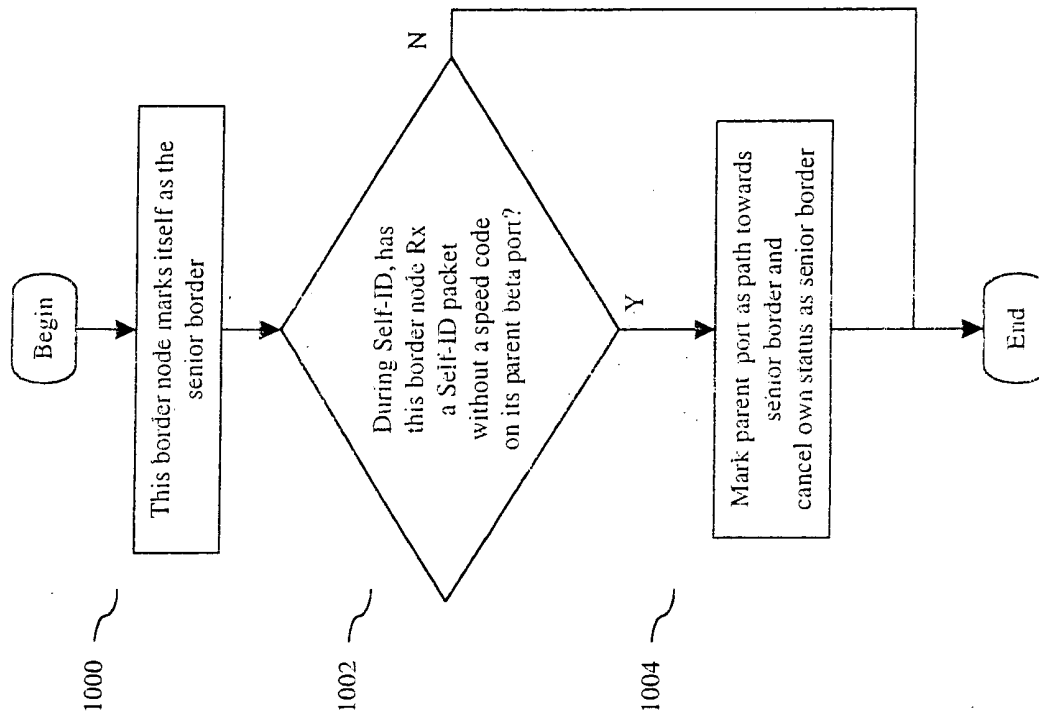


Fig. 10
Detection of Path to Senior Border
By a Border PHY
According to the Present Invention

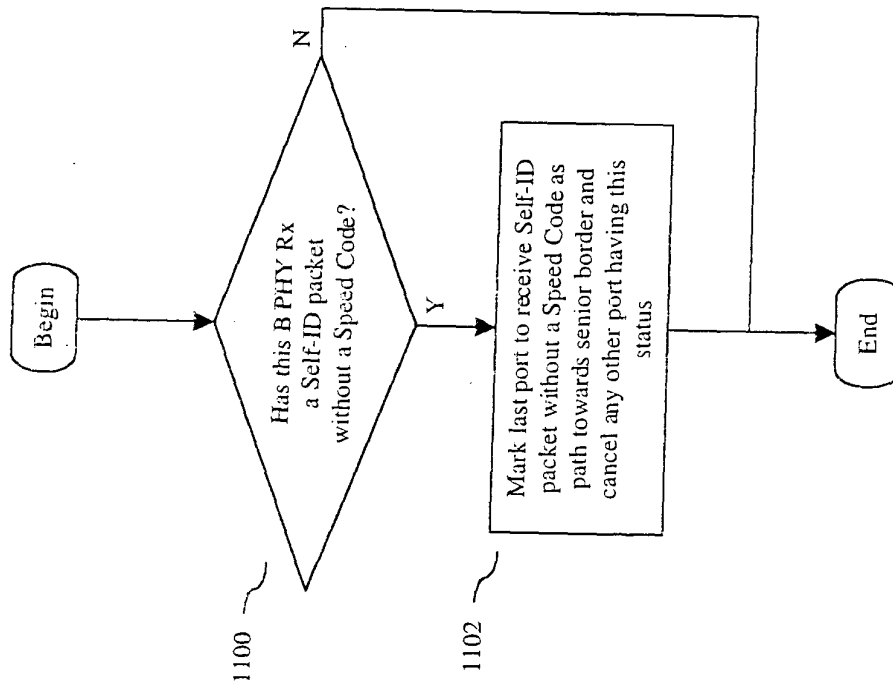


Fig. 11
Identification of Senior Border Node
By a B PHY
According to the Present Invention

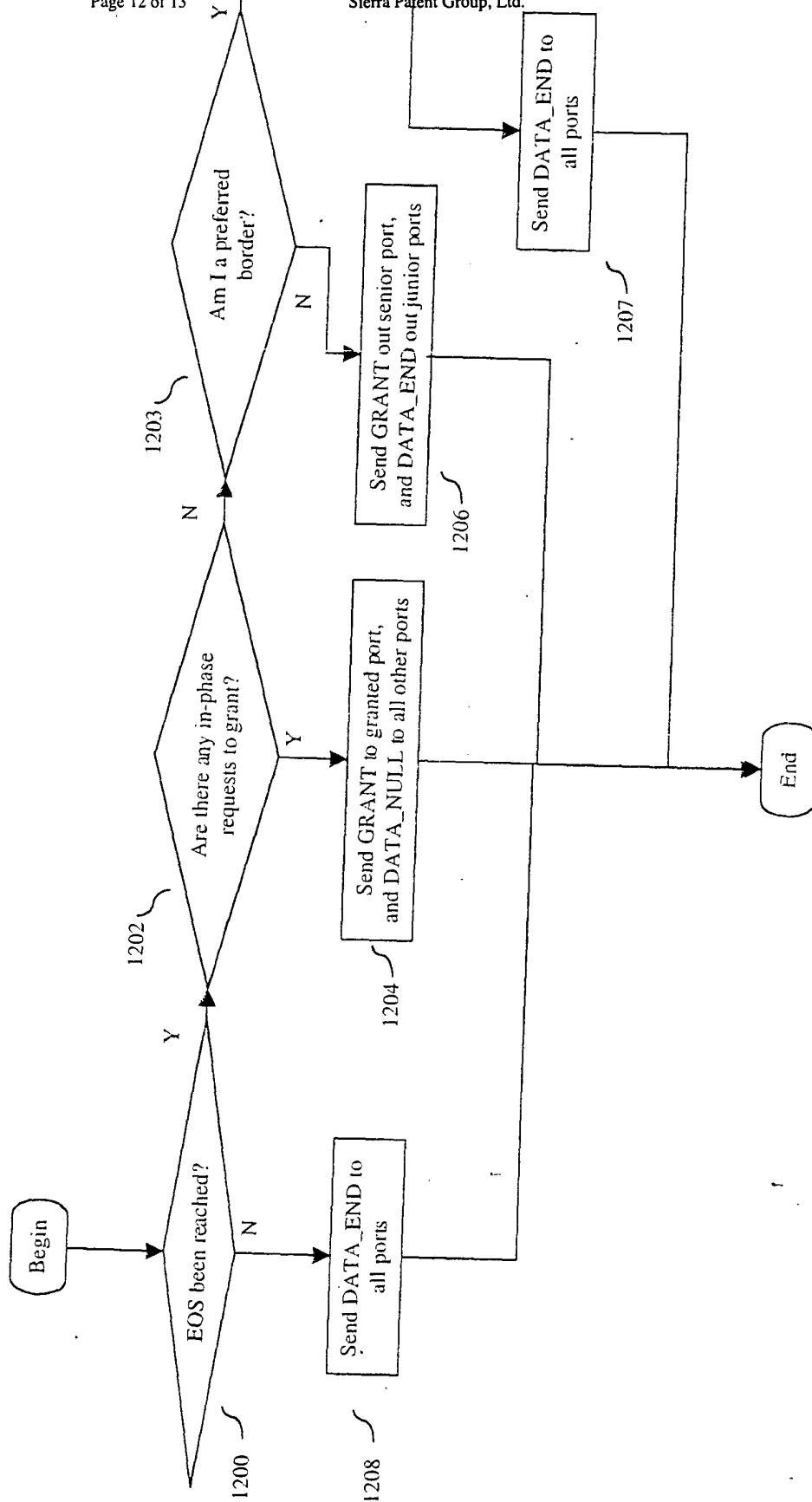


Fig. 12
 Restoring Senior Border as BOSS
 According to the Present Invention

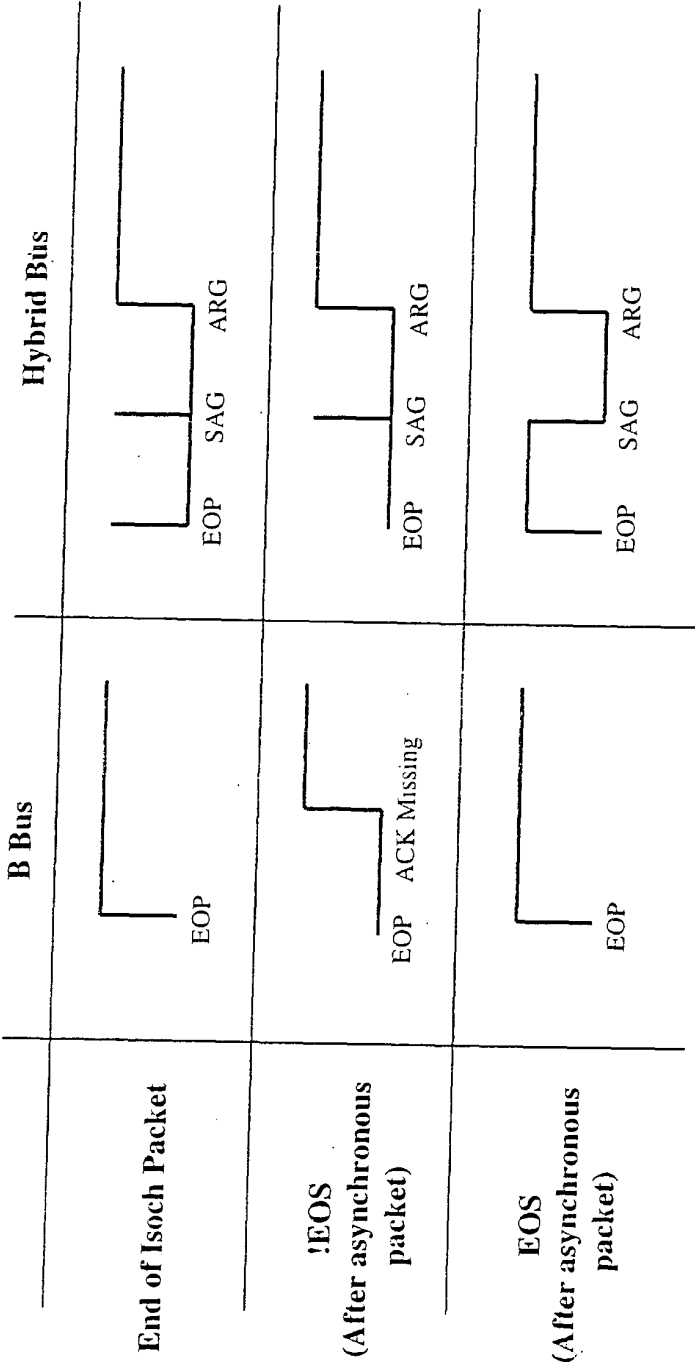


Fig. 13
OK_TO_GRANT Profile
According to the Present Invention